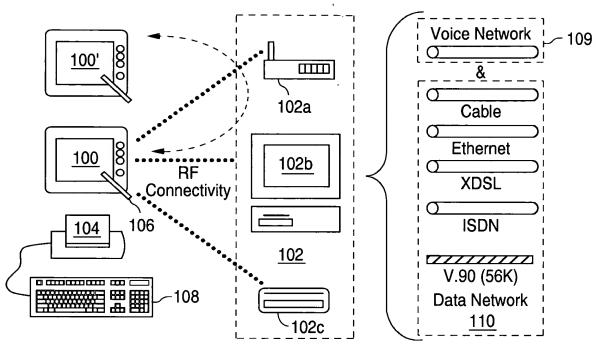


FIG. 7





955 4

FIG. 2

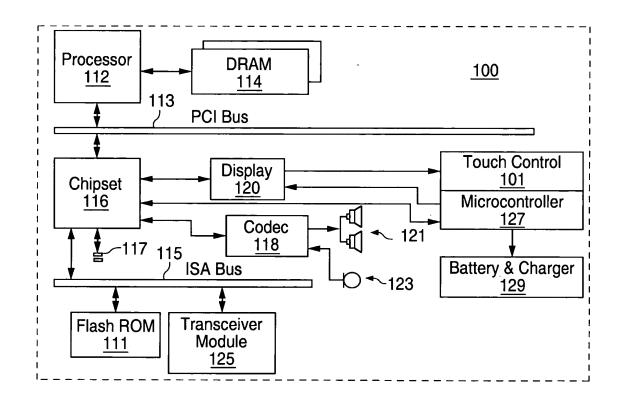


FIG. 3



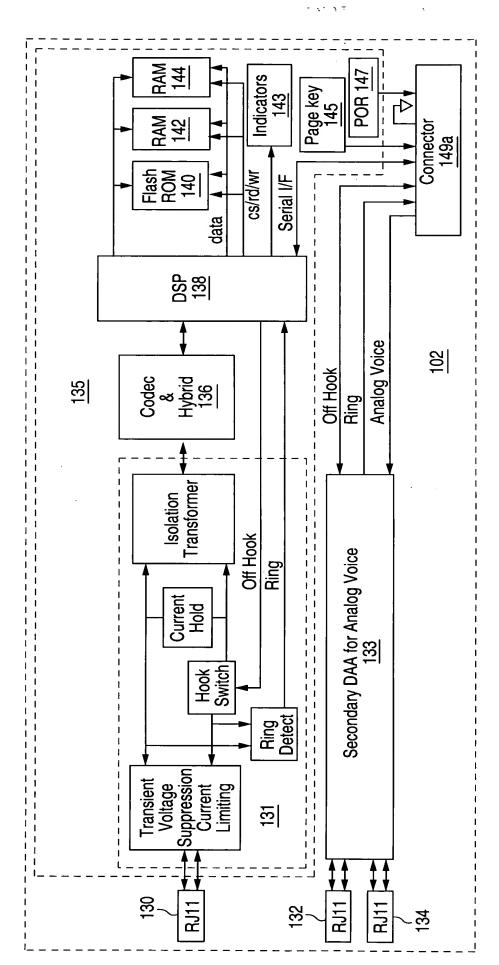


FIG. 4

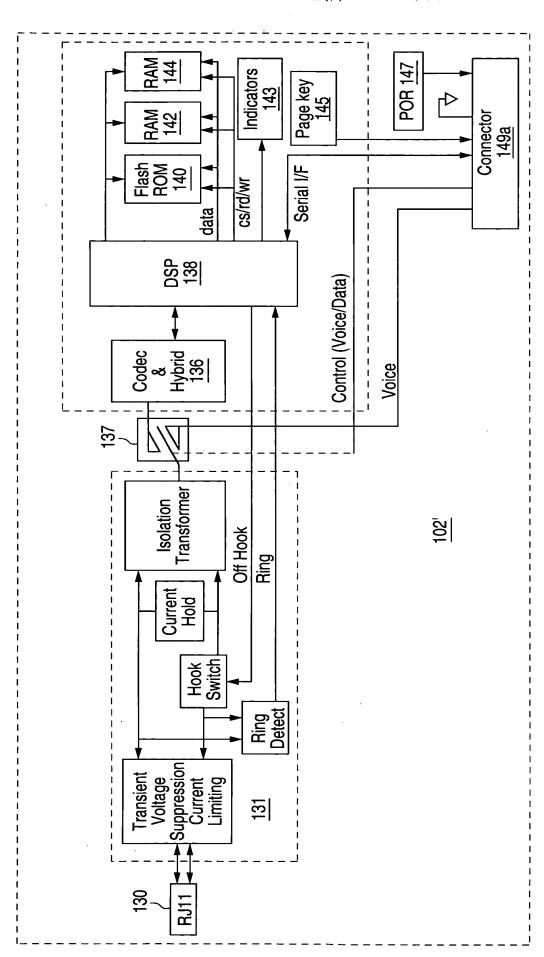


FIG. 5



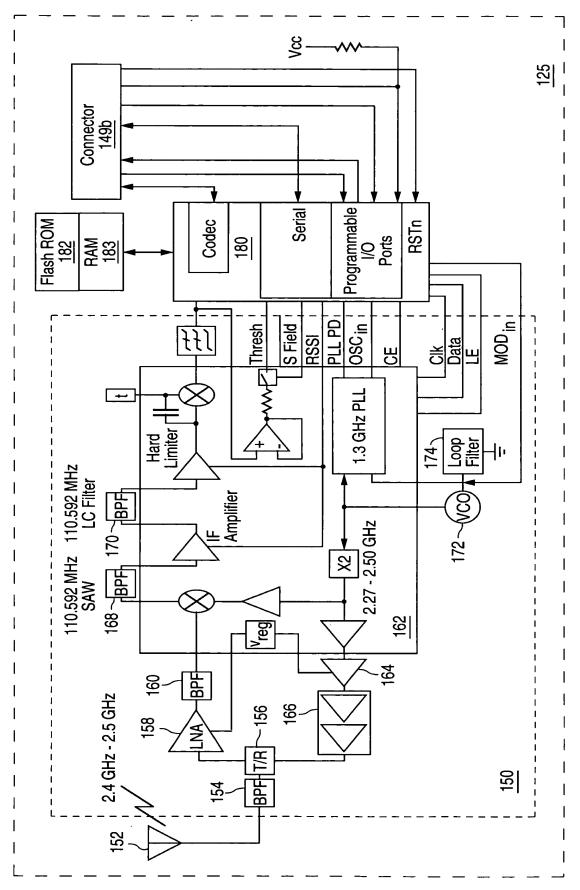
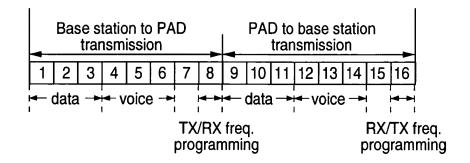
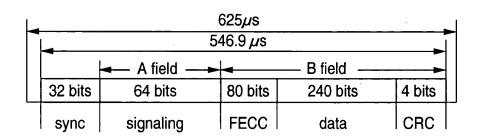


FIG. 6







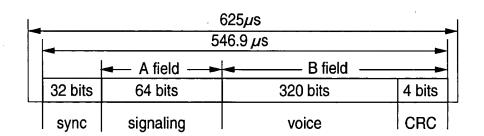


FIG. 8

PREAMBLE		HEADER			DATA
SYNC WORD 80 BITS	FRAME TIMING 16 BITS	DATA WORD LENGTH 12 BITS	DATA RATE 4 BITS	ERROR CHECK WORD 16 BITS	DATA 1-4095 BYTES
128 BITS					

FIG. 9 (PRIOR ART)